WEST

Freeform Search

Database:
US Patents Full-Text Database
US Pre-Grant Publication Full-Text Database
JPO Abstracts Database
EPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

Term:

Display: 10 **Documents in Display Format:** TI **Starting with Number** 1

Generate: ○ Hit List ◉ Hit Count ○ Side by Side ○ Image

Search | Clear | Help | Logout | Interrupt

Main Menu | Show S Numbers | Edit S Numbers | Preferences | Cases

Search History

DATE: Saturday, May 31, 2003 Printable Copy Create Case

Set Name side by side	Query	Hit Count	Set Name result set
	DB=USPT; PLUR=YES; OP=ADJ		
L8	L7 and ((table or map$4) near8 (access$4 near4 (right or permi$6 or grant$4)))	38	L8
L7	L6 and ((ID or identif$8 or descri$6) near6 (stor$3 or input$3 or obtain$4 or extract$4 or retriev$4))	849	L7
L6	L5 and ((table or map$4) near8 (ID or identif$8 or descri$6))	1129	L6
L5	L4 and ((task or process or resource or application) near8 (ID or identif$8 or descri$6))	2453	L5
L4	(address near6 (expan$4 or exten$4))	9106	L4
L3	l2 and (address near4 (expan$4 or exten$4))	0	L3
L2	L1 and (table or descri$6 or identif$6 or ID or register)	4	L2
L1	(5469556 or 5758060 or 5805879 or 6091414).pn.	4	L1

END OF SEARCH HISTORY

Set Name side by side	Query	Hit Count	Set Name result set
	DB=USPT; PLUR=YES; OP=ADJ		
L17	L16 and ((task or process) near4 access$4 near8 (ID or identification or identify$4 or descri$6))	28	L17
L16	(cpu or processor) near4 (ID or identification or identify$4 or descri$6) near8 (extract$4 or retriev$4 or obtain$4 or remov$4)	823	L16
L15	(cpu or processor) near4 (ID or identification or identify$4 or descri$6) near4 (extract$4 or retriev$4 or obtain$4 or remov$4)	639	L15
L14	L13 and l6	9	L14
L13	L12 and l11	260	L13
L12	address near4 (exten$4 or expan$4)	7116	L12
L11	L10 and ((previous$2 or prior or before) near4 (address or ID or identification or identify$4 or descri$6))	1090	L11
L10	L9 and l8 and l7	1985	L10
L9	table near8 (defin$6 or right or grant$4 or permi$6)	54904	L9
L8	(register or buffer or storage or memory) near8 (ID or identification or identify$4 or descri$6)	72579	L8
L7	((task or process) near4 (ID or identification or identify$4 or descri$6))	89263	L7
L6	L4 or l2	2211	L6
L5	L4 and l2	3	L5
L4	((709/102)!.CCLS.)	510	L4
L3	((709/109)!.CCLS.)	0	L3
L2	((711/2 |711/154 |711/163 |711/164)!.CCLS.)	1704	L2
L1	(711/2,154, 163, 163).ccls. or ((709/109)!.CCLS.)	0	L1

END OF SEARCH HISTORY

Set Name side by side	**Query**	**Hit Count**	**Set Name** result set
	DB=PGPB,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=ADJ		
L10	L9 and ((task or process or thread or resource) near4 (ID or identification or identif$6 or descri$6))	7	L10
L9	((right or permi$6 or grant$3 or privileg$4) near8 ((interrupt$4 or exception or fault) near4 number))	106	L9
	DB=USPT; PLUR=YES; OP=ADJ		
L8	L7 and ((table or map$4) near8(defin$6 or right or grant$3 or permi$6))	8	L8
L7	L6 and ((task or process or thread or resource) near4 (ID or identification or identif$6 or descri$6))	19	L7
L6	((right or permi$6 or grant$3 or privileg$4) near8 ((interrupt$4 or exception or fault) near4 number))	183	L6
	DB=PGPB,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=ADJ		
L5	L4 and ((right or permi$6 or grant$3 or privileg$4) near8 ((interrupt$4 or exception or fault) near4 number))	1	L5
L4	(address adj2 space) near4 (exten$4 or expan$4)	403	L4
	DB=USPT; PLUR=YES; OP=ADJ		
L3	L1 and ((right or permi$6 or grant$3 or privileg$4) near8 ((interrupt$4 or exception or fault) near4 number))	3	L3
L2	L1 and (((access$4 or read$4 or writ$4 or stor$3 or load$3) near3 (right or permi$6 or grant$3 or privileg$4)) near8 ((interrupt$4 or exception) near4 number))	0	L2
L1	(address adj2 space) near4 (exten$4 or expan$4)	583	L1

END OF SEARCH HISTORY

Search Results

Search Results for: [((address space) <near/4> (exten* or expan*)) and ((task or process or thread or resource) <near/4> (ID or identif* or descri*)) and ((table or map*) <near/8> (defin* or right or grant* or permi*))]
Found 5 of 110,773 searched. → Rerun within the Portal

Search within Results

GO > Advanced Search > Search Help/Tips

Sort by: Title Publication Publication Date Score Binder

Results 1 - 5 of 5 short listing

1 A new page table for 64-bit address spaces 99%
M. Talluri , M. D. Hill , Y. A. Khalidi
ACM SIGOPS Operating Systems Review , Proceedings of the fifteenth ACM symposium on Operating systems principles December 1995
Volume 29 Issue 5

2 Self-spacial join selectivity estimation using fractal concepts 99%
Alberto Belussi , Christos Faloutsos
ACM Transactions on Information Systems (TOIS) April 1998
Volume 16 Issue 2

The problem of selectivity estimation for queries of nontraditional databases is still an open issue. In this article, we examine the problem of selectivity estimation for some types of spatial queries in databases containing real data. We have shown earlier [Faloutsos and Kamel 1994] that real point sets typically have a nonuniform distribution, violating consistently the uniformity and independence assumptions. Moreover, we demonstrated that the theory of ...

3 An in-cache address translation mechanism 99%
D. A. Wood , S. J. Eggers , G. Gibson , M. D. Hill , J. M. Pendleton
ACM SIGARCH Computer Architecture News , Proceedings of the 13th annual international symposium on Computer architecture June 1986
Volume 14 Issue 2

In the design of SPUR, a high-performance multiprocessor

workstation, the use of large caches and hardware-supported cache consistency suggests a new approach to virtual address translation. By performing translation in each processor's virtually-tagged cache, the need for separate translation lookaside buffers (TLBs) is eliminated. Eliminating the TLB substantially reduces the hardware cost and complexity of the translation mechanism and eliminates the translation consistency problem. Trac ...

4 An analysis of errors and their causes in system programs 98%

Albert Endres

ACM SIGPLAN Notices , Proceedings of the international conference on Reliable software April 1975

Volume 10 Issue 6

Program errors detected during internal testing of the operating system DOS/VS form the basis for an investigation of error distributions in system programs. Using a classification of the errors according to various attributes, conclusions can be drawn concerning the possible causes of these errors. The information thus obtained is applied in a discussion of the most effective methods for the detection and prevention of errors.

5 801 storage: architecture and programming 98%

Albert Chang , Mark F. Mergen

ACM Transactions on Computer Systems (TOCS) February 1988

Volume 6 Issue 1

Based on novel architecture, the 801 minicomputer project has developed a low-level storage manager that can significantly simplify storage programming in subsystems and applications. The storage manager embodies three ideas: (1) large virtual storage, to contain all temporary data and permanent files for the active programs; (2) the innovation of database storage, which has implicit properties of access serializability and atomic update, similar to those o ...

Results 1 - 5 of 5 **short listing**

Your search matched **[0]** of **[942182]** documents.

You may refine your search by editing the current search expression or entering
a new one the text box. Then click search Again.

((address space) <near/4> (exten$ or expan$)) and ((task or process or thread or resour

Search Again

OR

Use your browser's back button to return to your original search page.

Results:

No documents matched your query.